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PATENT #3

GP 2822

IDS  
10-2-01  
R. Stiles

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of : Pervez H. Sagarwala, et al.  
U.S. Serial No. : 09/800,039  
Filed : March 6, 2001  
For : CMOS INTEGRATED CIRCUIT DEVICE WITH LDD N-CHANNEL TRANSISTOR AND NON-LDD P-CHANNEL TRANSISTOR  
Group No. : 2822  
Examiner : (Not Yet Assigned)

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Commissioner of Patents  
and Trademarks  
Washington, D. C. 20231

<p align="center"><b>CERTIFICATE OF MAILING</b></p> <p>I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner of Patents and Trademarks, Washington, D.C. 20231, on <u>May 21, 2001</u>.</p> <p><u>KATHY LONGENECKER</u> (Printed or typed name of person signing the certificate)</p> <p><u>Kathy Longenecker</u> (Signature of the person signing the certificate)</p>
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Dear Sir:

INFORMATION DISCLOSURE STATEMENT

Pursuant to the duty of disclosure under 37 C.F.R. § 1.56, Applicant submits this statement. This submittal is made in accordance with 37 C.F.R. §§ 1.97 and 1.98 and § 609 of the Manual of Patent Examining Procedure. The patents, publications and other information herein are listed below and on the attached Form PTO-1449. Copies of the listed references are submitted herewith.


<u>U.S. Patent No.</u>	<u>Inventor</u>	<u>Date</u>
4,891,326	Koyanagi	Jan. 2, 1990
5,087,582	Campbell, et al.	Feb. 11, 1992
5,098,855	Komori, et al.	Mar. 24, 1992
5,266,823	Noji, et al.	Nov. 30, 1993
5,273,914	Miyajima, et al.	Dec. 28, 1993
5,291,052	Kim, et al.	Mar. 1, 1994
5,324,974	Liao	June 28, 1994
5,668,024	Tsai, et al.	Sep. 16, 1997
5,766,991	Chen	June 16, 1998

Applicant hereby expressly reserves the right to swear behind the effective dates of any of the above Patents and to question the relevance and materiality of the Patents and Publications listed herein, in whole, in part, or in combination, subsequent to filing this Information Disclosure Statement.

Respectfully submitted,

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Date: 5-21-01

  
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